Docket No. 201163US-2S

IN RE APPLICATION OF: JUNICHI ASADA

SERIAL NO: 09/740,902

FILED:

DECEMBER 21, 2000

FOR:

SEMICONDUCTOR DEVICE

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

Transmitted herewith is an amendment in the above-identified application.

- No additional fee is required
- □ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- Additional documents filed herewith:

Marked-Up Copy

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	2	MINUS	20	0	× \$18 =	\$0.00
INDEPENDENT	2	MINUS	3	0	× \$84 =	\$0.00
		□ MUL	TIPLE DEPENDENT	CLAIMS	+ \$280 =	\$0.00
		TOTAL OF ABOVE CALCULATIONS				\$0.00
		□ Reduction by 50% for filing by Small Entity				\$0.00
		□ Recor	dation of Assignment		+ \$40 =	\$0.00
					TOTAL	\$0.00

☐ A check in the amount of

is attached.

- Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

"RESPONSE UNDER 37 CFR 1.116-

EXPEDITED PROCEDURE EXAMINING GROUP 2815

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201163US-620-39-2S

AMENDMENT UNDER 37 C.F.R. §1.116 EXPEDITED PROCEDURE EXAMINING GROUP

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

JUNICHI ASADA

: EXAMINER: CHU, C. C.

SERIAL NO: 09/740,902

FILED: DECEMBER 21, 2000

: GROUP ART UNIT: 2815

TECHNOLOGY CENTER 2800

FOR: SEMICONDUCTOR DEVICE

AMENDMENT UNDER 37 C.F.R. §1.116

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

In response to the Office Action dated April 11, 2002, please amend this application 12010 ws:

as follows:

IN THE CLAIMS

Please cancel Claims 1-3, 8, 6, 9, 11 and 13 without prejudice.

Please add new Claims 28 and 29 as shown below:

28. (New) Asemiconductor memory device comprising:

a semiconductor element;

a plurality of lead wires connected to a plurality of connecting electrodes formed on

said semiconductor element;

at least a single dummy lead wire that is not electrically connected to said

semiconductor element and does not include an outer lead portion for electrically connecting